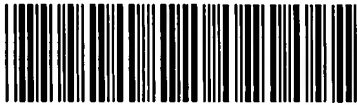


Search Notes

Application/Control No.

10/603,252

Examiner

Jesse Diller

Applicant(s)/Patent under
Reexamination

HELLER, THOMAS J.

Art Unit

2187

SEARCHED

Class	Subclass	Date	Examiner

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
NPL/East text search: "dynamic cache coherency region", supervisor, hypervisor, logical partition, deferred purge/flush/evict	4/7/2007	JD
East Patent search: 711/118. combined text/class search with 709, 712	4/5/2007	JD
East/eDAN inventor name search	4/4/2007	JD
Consulted Steven Elmore, AU 2185	4/7/2007	JD
Consulted Reba Elmore, AU 2189	4/7/2007	JD
Consulted Brian Peugh, AU 2187	4/8/2007	JD